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NOTICE OF ALLOWANCE AND FEE(S) DUE

27572 7590 05/29/2009

HARNESS, DICKEY & PIERCE, P.L.C.
P.O. BOX 828
BLOOMFIELD HILLS, MI 48303

EXAMINER

LOO, JUVENA W

ART UNIT

PAPER NUMBER

2416

DATE MAILED: 05/29/2009

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/722,021	11/25/2003	Mark Andrew Whittaker Stewart	1400B-000039/US	6538

TITLE OF INVENTION: CONNECTION CONTROLLER

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1510	\$300	\$0	\$1810	08/31/2009

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

B. If the status above is to be removed, check box 5b on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and twice the amount of the ISSUE FEE shown above, or

If the SMALL ENTITY is shown as NO:

A. Pay TOTAL FEE(S) DUE shown above, or

B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

II. PART B - FEE(S) TRANSMITTAL, or its equivalent, must be completed and returned to the United States Patent and Trademark Office (USPTO) with your ISSUE FEE and PUBLICATION FEE (if required). If you are charging the fee(s) to your deposit account, section "4b" of Part B - Fee(s) Transmittal should be completed and an extra copy of the form should be submitted. If an equivalent of Part B is filed, a request to reapply a previously paid issue fee must be clearly made, and delays in processing may occur due to the difficulty in recognizing the paper as an equivalent of Part B.

III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

PART B - FEE(S) TRANSMITTAL

Complete and send this form, together with applicable fee(s), to: **Mail Stop ISSUE FEE**
Commissioner for Patents
P.O. Box 1450
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INSTRUCTIONS: This form should be used for transmitting the ISSUE FEE and PUBLICATION FEE (if required). Blocks 1 through 5 should be completed where appropriate. All further correspondence including the Patent, advance orders and notification of maintenance fees will be mailed to the current correspondence address as indicated unless corrected below or directed otherwise in Block 1, by (a) specifying a new correspondence address; and/or (b) indicating a separate "FEE ADDRESS" for maintenance fee notifications.

CURRENT CORRESPONDENCE ADDRESS (Note: Use Block 1 for any change of address)

27572 7590 05/29/2009
HARNESS, DICKEY & PIERCE, P.L.C.
P.O. BOX 828
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Note: A certificate of mailing can only be used for domestic mailings of the Fee(s) Transmittal. This certificate cannot be used for any other accompanying papers. Each additional paper, such as an assignment or formal drawing, must have its own certificate of mailing or transmission.

Certificate of Mailing or Transmission

I hereby certify that this Fee(s) Transmittal is being deposited with the United States Postal Service with sufficient postage for first class mail in an envelope addressed to the Mail Stop ISSUE FEE address above, or being facsimile transmitted to the USPTO (571) 273-2885, on the date indicated below.

(Depositor's name)

(Signature)

(Date)

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10/722,021	11/25/2003	Mark Andrew Whittaker Stewart	1400B-000039/US	6538

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nonprovisional	NO	\$1510	\$300	\$0	\$1810	08/31/2009
EXAMINER	ART UNIT	CLASS-SUBCLASS				
LOO, JUVENA W	2416	370-389000				

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

- Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.
 "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. **Use of a Customer Number is required.**

2. For printing on the patent front page, list

- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
(2) the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.

1 _____
2 _____
3 _____

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

4a. The following fee(s) are submitted:

- Issue Fee
 Publication Fee (No small entity discount permitted)
 Advance Order - # of Copies _____

4b. Payment of Fee(s): (Please first reapply any previously paid issue fee shown above)

- A check is enclosed.
 Payment by credit card. Form PTO-2038 is attached.
 The Director is hereby authorized to charge the required fee(s), any deficiency, or credit any overpayment, to Deposit Account Number _____ (enclose an extra copy of this form).

5. Change in Entity Status (from status indicated above)

- a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

NOTE: The Issue Fee and Publication Fee (if required) will not be accepted from anyone other than the applicant; a registered attorney or agent; or the assignee or other party in interest as shown by the records of the United States Patent and Trademark Office.

Authorized Signature _____

Date _____

Typed or printed name _____

Registration No. _____

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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27572	7590	05/29/2009	EXAMINER	
HARNESS, DICKEY & PIERCE, P.L.C. P.O. BOX 828 BLOOMFIELD HILLS, MI 48303				LOO, JUVENA W
ART UNIT		PAPER NUMBER		
2416				DATE MAILED: 05/29/2009

Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 849 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 849 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (<http://pair.uspto.gov>).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 or (571)-272-4200.

Notice of Allowability	Application No.	Applicant(s)	
	10/722,021	WHITTAKER STEWART, MARK ANDREW	
	Examiner	Art Unit	
	JUVENA LOO	2416	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS**. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. This communication is responsive to 01/15/2009.

2. The allowed claim(s) is/are 1-13 and 15-21.

3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).

a) All b) Some* c) None of the:

1. Certified copies of the priority documents have been received.

2. Certified copies of the priority documents have been received in Application No. _____.

3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.

5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.

(a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
1) hereto or 2) to Paper No./Mail Date _____.

(b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of
Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).

6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. Notice of References Cited (PTO-892)

5. Notice of Informal Patent Application

2. Notice of Draftsperson's Patent Drawing Review (PTO-948)

6. Interview Summary (PTO-413),
Paper No./Mail Date _____.

3. Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____.

7. Examiner's Amendment/Comment

4. Examiner's Comment Regarding Requirement for Deposit
of Biological Material

8. Examiner's Statement of Reasons for Allowance

9. Other _____.

Allowable Subject Matter

1. Claims 1-13 and 15-21 are allowed.
2. The following is an examiner's statement of reasons for allowance:

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

The present invention is directed to a strictly non-interfering network (SNIN). According to the description of a strictly non-interfering network (SNIN) in section 0025, page 5 of the specification (dated December 28, 2007) as well as the description of a strictly non-interfering network (SNIN) provided in the page 13 of the Remark (dated December 16, 2008). A strictly non-interfering network is a network for which the only queuing delays experienced by an admissible traffic pattern are attributable to the multiplexing of packets from slow links onto a faster link whose aggregate bandwidth at least equals the sum of the bandwidths of the smaller links. In a SNIN, competing traffic sources do not attempt to use the same network resources at the same time. The implementation of a SNIN requires that resources be dedicated through the network in

support of an active communication session. In order to accomplish this, non-blocking networks can be used. Each independent claim (claims 1, 8 and 13) identifies the uniquely distinct features:

Regarding claim 1, a connection controller for a network comprising a plurality of first stage switches, a plurality of second stage switches coupled to each of the plurality of first stage switches, and a plurality of packet sources coupled to the plurality of first stage switches and each configured to request a traffic pattern for a packet, the connection controller comprising:

a network topology cache capable of being coupled to the network and configured to receive network topology data from the network;

a packing algorithm circuit coupled to the network topology cache and capable of being coupled to the plurality of packet sources, the packing algorithm circuit configured to:

receive the network topology data from the network topology cache, receive the traffic pattern request from each of the plurality of packet sources in a predetermined time window, and

*compute an actual traffic pattern for the packet based on the received network topology data and all the received traffic pattern requests, wherein the actual traffic pattern comprises one of the plurality of first stage switches and one of the plurality of second stage switches such that the network is able to operate as a **strictly non-interfering network**; and*

a logical network state entity coupled to the packing algorithm circuit and capable of being coupled to the packet source, the logical network state entity configured to communicate the computed actual traffic pattern to the source.

Regarding claim 8, a connection controller for a network comprising a plurality of first stage switches including a forwarding table, a plurality of second stage switches including a forwarding table and coupled to each of the plurality of first stage switches, and a plurality of nodes coupled to the plurality of first stage switches, the connection controller comprising a computer-readable medium containing computer instructions for a processor that when executed by the processor, cause the processor to perform a method comprising the steps of:

calculating a plurality of routing trees, each routing tree comprising the plurality of first stage switches, and one of the plurality of second stage switches;

assigning a Destination Location Identifier (DLID) to each routing tree and to each node;

calculating a set of forwarding instructions for each of the plurality of first stage switches and each of the plurality of second stage switches based on the assigned DLIDs, wherein the set of forwarding instructions causes the one of the plurality of first stage switches and the one of the plurality of second stage switches in each routing tree to operate in a manner that creates a path between each of the plurality of nodes;

populating the forwarding table of each of the plurality of first stage switches and the plurality of second stage switches with the assigned DLIDs and the set of forwarding instructions; and

*computing and assigning a DLID to a packet to be transferred in the network based on all traffic pattern requests from the plurality of nodes in a predetermined time window such that the network operates as a **strictly non-interfering network**.*

Regarding claim 13, a computer-readable medium containing computer instructions for instructing a processor to perform a method for forwarding a packet from a source node of a plurality of nodes to a destination node of the plurality of nodes within a network comprising a plurality of first stage switches assigned a respective DLID, and a plurality of second stage switches coupled to each of the plurality of first stage switches and assigned a respective DLID, wherein a first switch of the plurality of first stage switches is further coupled to the source node and a second switch of the plurality of first stage switches is coupled to the destination node, the computer instructions, when executed by the processor, cause the processor to perform a method comprising the steps of:

*computing and associating a destination node DLID with the packet based on the destination node and all traffic pattern requests from the plurality of nodes in a predetermined time window such that the network operates as a **strictly non-interfering network**; and*

routing the packet along a path through the first switch of the plurality of first stage switches, one of the plurality of second stage switches, and the second switch of the plurality of first stage switches to the destination based on the first switch DLID, the DLID of the one of the plurality of second stage switches, and the second switch DLID, wherein the first switch of the plurality of first stage switches, the one of the plurality of second stage switches, and the second switch of the plurality of first stage switches forward the packet according to the destination node DLID associated with the packet such that the network operates as a strictly non-interfering network.

The closest prior arts, Foster, Dell, and Karp disclose a conventional multi-stage switching network that generates a routing path for a packet, after a request is received, based on network topology data, either singularly or in combination, fail to anticipate or render the above features obvious.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to JUVENA LOO whose telephone number is (571)270-1974. The examiner can normally be reached on Monday - Friday: 7:30am-4:00pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kwang Yao can be reached on (571) 272-3182. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/JUVENA LOO/
Examiner
Art Unit 2416
May 16, 2009

/KWANG B. YAO/
Supervisory Patent Examiner, Art Unit 2416

Application/Control Number: 10/722,021
Art Unit: 2416

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